

PROOF COMPLEXITY AND INTERPOLATION PCI I

PROPOSITIONAL PROOF COMPLEXITY

Study of certificates of unsatisfiability for CNF formulas (and more, but this definition is good enough for today)

PROOF SYSTEM for language L

Deterministic algorithm $P(x, \pi)$

Polynomial-time in $|x| + |\pi|$

Completeness If $x \in L$, then $\exists \pi$ s.t. $P(x, \pi) = 1$

Soundness If $x \notin L$, then $\forall \pi$ $P(x, \pi) = 0$

A proof system P is POLYNOMIALLY BOUNDED if \exists polynomial p s.t. $\forall x \in L \exists$ proof π s.t. $|\pi| \leq p(|x|)$ and $P(x, \pi) = 1$

A PROPOSITIONAL PROOF SYSTEM is a proof system for $UNSAT = \{ \text{Unsatisfiable CNF formulas} \}$

Believe that no polynomially bounded propositional proof system exist, because this would imply $NP = coNP$. Converse is also true: If $NP = coNP$, then \exists polynomially bounded proof system for UNSAT (namely, standard NP verifier)

THEOREM 1.3 [Cook-Reckhow '79]

If there are no polynomially bounded propositional proof systems, then $P \neq NP$

Proof. UNSAT is in coNP. If no polynomially bounded proof system, then UNSAT \notin NP (since NP is exactly family of languages with polynomially bounded proof systems), and so coNP \neq NP. But then NP \neq P, because P is closed under complement

"COOK'S PROGRAM"

Prove superpolynomial lower bounds for stronger and stronger proof systems until one day we can get general lower bound for all proof systems.

Has not worked out so well

But many other reasons to study proof complexity, e.g.:

- Compare strengths of different types of mathematical reasoning (formalized in different proof systems)
- Understand power and limitations of different algorithmic paradigms (formalized as different proof systems)

Today we will study the RESOLUTION proof system - arguably the most investigated proof system in all of proof complexity

Introduced in 1937 (?) by Blake
Started to get attention in 1960s in the context of Boolean satisfiability algorithms, so-called SAT solvers

- [Davis - Putnam 1960]
- [Davis - Logemann - Loveland 1962]
- [Robinson 1965]

Resolution is still the basis of state-of-the-art solvers using CONFLICT-DRIVEN CLAUSE LEARNING, invented by Marques-Silva & Sakallah 1996

PLAN FOR TODAY (AND NEXT LECTURE)

- Define resolution proof system
- Show how short resolution proofs can be turned into small circuits using INTERPOLATION
- Use the lower bound for monotone circuits computing CLIQUE_n that we just proved in the last couple of lectures to get strong lower bounds for a related family of CNF formulas

RESOLUTION

- Start with clauses in formula F
- Derive new clauses that are semantically implied
- End by deriving contradiction
- Then original formula F must have been contradictory, i.e., unsatisfiable

DEF Resolution refutation π of unsatisfiable CNF formula F , denoted $\pi: F \vdash \perp$, is a sequence of clauses

$$\pi = (D_1, D_2, D_3, \dots, D_{l-1}, D_l)$$

such that $D_l = \perp$ is the empty clause not containing any literals and each D_i is

- an AXIOM CLAUSE $D_i \in F$, or
- a clause on the form $D_i = B \vee C$ derived from clauses $D_j = B \vee x$ and $D_k = C \vee \bar{x}$ for $j, k < i$ using the RESOLUTION RULE

$$\frac{B \vee x \quad C \vee \bar{x}}{B \vee C}$$

($B \vee x$ and $C \vee \bar{x}$ are RESOLVED OVER x and $B \vee C$ is the RESOLVENT)

View clauses as sets of literals: no repetition, and order does not matter

Without loss of generality, all clauses are non-trivial - never $x \in C$ and $\bar{x} \in C$
 (Not hard to show that trivial clauses can be removed from any resolution refutation)

LEMMA 2.2 Resolution is sound and complete, i.e., there exists a resolution refutation of F if and only if F is unsatisfiable.

Proof sketch (\Rightarrow) Suppose α satisfies all clauses in F . Then α satisfies all resolvents by induction. But no assignment can satisfy the empty clause without literals.

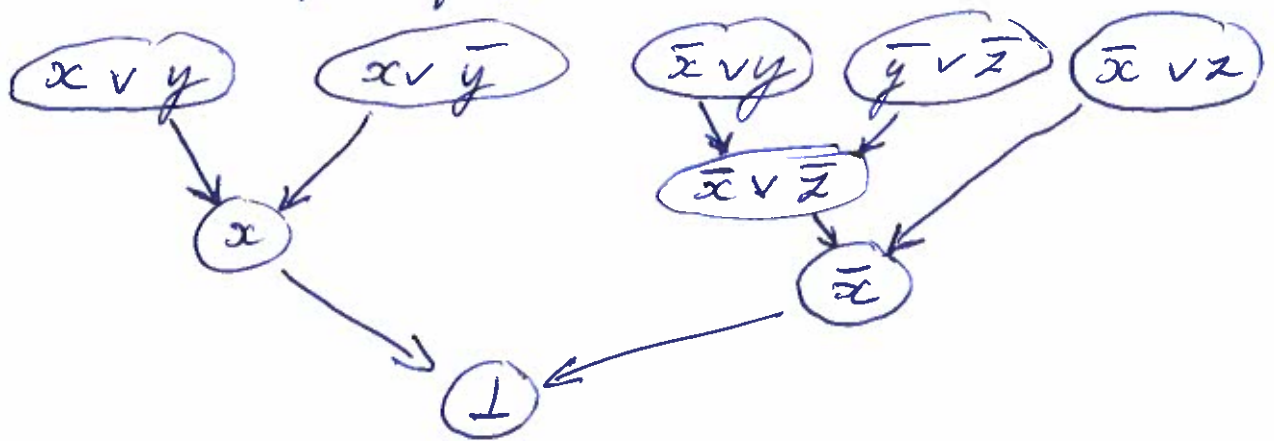
(\Leftarrow) Not hard, but requires an argument. Left as an exercise.

Example $F = (x \vee y) \wedge (x \vee \bar{y}) \wedge (\bar{x} \vee y) \wedge (\bar{x} \vee z) \wedge (\bar{y} \vee \bar{z})$

Present refutation as annotated list

1	$x \vee y$	axiom
2	$x \vee \bar{y}$	axiom
3	$\bar{x} \vee y$	axiom
4	$\bar{x} \vee z$	axiom
5	$\bar{y} \vee \bar{z}$	axiom
6	x	Res (1, 2)
7	$x \vee \bar{z}$	Res (3, 5)
8	\bar{x}	Res (4, 7)
9	\perp	Res (6, 8)

Can also represent resolution refutation π PCI VI
 as directed acyclic graph (DAG) G_π



That is:

- One node per clause
- Axioms turn into source nodes
- Edges from resolved clauses to resolvents

LENGTH of resolution refutation $\pi =$
 $= \# \text{ clauses in it } \quad L(\pi) \quad [= 9 \text{ in our example}]$

LENGTH OF REFUTING F

$$L_R(F \vdash \perp) = \min_{\pi: F \vdash \perp} \{ L(\pi) \}$$

Often also called the size of a resolution refutation. Size measure should arguably count also the number of literals in each clause, but this is at most a linear factor difference, and we mainly care about difference between polynomial and super-polynomial

INTERPOLATION AND CLIQUE-COLOURING FORMULAS PCI VII

Interpolation method introduced by
Krajíček 1994

Used by Pudlák in 1997 to prove lower bounds on proof length for clique-colouring formulas in the CUTTING PLANES proof system (much stronger proof system).

To make our lives a little bit easier, we will do a version of Pudlák's result for resolution, which is a weaker proof system

Clique-colouring formulas
Parameters

- n : # vertices in graph
- m : size of clique

Formula says:

"There exists a graph on n vertices which has an m -clique and is also $(m-1)$ -colourable"

Obviously absurd, so formula unsatisfiable
But resolution has a hard time understanding this...

Variables

$$\vec{P} = \{P_{ij} \mid 1 \leq i < j \leq n\}$$

P_{ij} = "there is an edge between vertices i & j "

$$\vec{q} = \{q_{k,i} \mid k \in [m], i \in [n]\}$$

$q_{k,i}$ = "vertex i is k th member of clique"

$$\vec{r} = \{r_{i,l} \mid i \in [n], l \in [m-1]\}$$

$r_{i,l}$ = "vertex i gets colour l "

Axiom clauses

$\bigvee_{i \in [n]} q_{k,i}$ "some vertex is k th clique member"

$\overline{q_{k,i}} \vee \overline{q_{k',i}}$ "clique vertices have unique member numbers" ($k \neq k'$)

$P_{ij} \vee \overline{q_{k,i}} \vee \overline{q_{k',j}}$ "clique members are connected by edges" ($i < j, k \neq k'$)

$\bigvee_{l \in [m-1]} r_{i,l}$ "every vertex gets a colour"

$\overline{P_{ij}} \vee \overline{r_{i,l}} \vee \overline{r_{j,l}}$ "neighbours have distinct colours"

Clique-colouring formula can be written as

$$A(\vec{p}, \vec{q}) \wedge B(\vec{p}, \vec{r}) \quad (*)$$

where the variable sets $\vec{p}, \vec{q}, \vec{r}$ are disjoint.

Given partial assignment, or restriction, \mathcal{g} to variables $\text{Vars}(F)$ of CNF formula F , we write F/\mathcal{g} for the restricted formula where variables $x \in \text{Dom}(\mathcal{g})$ are replaced by values $\mathcal{g}(x)$ and formula is simplified by removing

- satisfied clauses
- falsified literals

Ex

$$F = (x \vee y) \wedge (x \vee \bar{y}) \wedge (\bar{x} \vee y) \wedge (\bar{x} \vee z) \wedge (\bar{y} \vee \bar{z})$$

$$\mathcal{g} = \{y \mapsto 1\}$$

$$F/\mathcal{g} = x \wedge (\bar{x} \vee z) \wedge \bar{z}$$

Suppose we have unsatisfiable formula

$$F = A(\vec{p}, \vec{q}) \wedge B(\vec{p}, \vec{r}) \quad \text{as in } (*)$$

and restriction \mathcal{g} assigning all of \vec{p}

Then $F|_g = A(\vec{p}, \vec{q})|_g \wedge B(\vec{p}, \vec{r})|_g$ PCI \bar{X}

splits into two formulas on disjoint sets of variables.

Write

$$A(\vec{p}, \vec{q})|_g = A(g, \vec{q})$$

$$B(\vec{p}, \vec{r})|_g = B(g, \vec{r})$$

• for notational convenience

For any such g , either $A(g, \vec{q})$

• or $B(g, \vec{r})$ (or both) must be unsatisfiable.

A Boolean circuit $I(\vec{p})$ is an INTERPOLANT for $A(\vec{p}, \vec{q}) \wedge B(\vec{p}, \vec{r})$ if

$$I(g) = 0 \Rightarrow A(g, \vec{q}) \text{ unsatisfiable}$$

$$• \quad I(g) = 1 \Rightarrow B(g, \vec{r}) \text{ unsatisfiable}$$

• Such interpolants always exist (and are not necessarily unique)

We are going to prove

If $A(\vec{p}, \vec{q}) \wedge B(\vec{p}, \vec{r})$ has a short resolution refutation, then it also has a small interpolant.

But suppose interpolant computes function for which we have a circuit lower bound — then this shows that there cannot exist any short resolution refutations

PCI XI

PROOF STRATEGY

- ① Start with formula $F = A(\vec{p}, \vec{q}) \wedge B(\vec{p}, \vec{r})$
- ② Assume towards contradiction that F has short resolution refutation
- ③ Deduce that there exist small interpolants
- ④ But argue now that interpolant computes something for which we have circuit lower bound
- ⑤ Contradiction! Hence refutation cannot be short

Proof systems for which this proof strategy works are said to have FEASIBLE

INTERPOLATION. Resolution (and cutting planes) has feasible interpolation.

Use this to show that clique-colouring formulas are hard for resolution

PROBLEM: We don't have good general circuit lower bounds!

SOLUTION: There is a monotone version of interpolation, and we are lucky enough that this will work

We have proven:

THEOREM 5.1 [Razborov '85, Alon-Boppana '87]

Let undirected graph G on n vertices be represented by $\binom{n}{2}$ edge indicator bits.

Then for $m = \Theta(\sqrt[4]{n})$ there is no monotone circuit of size $2^{o(\sqrt{m})}$ that can distinguish between

- G has an m -clique
- G is $(m-1)$ -colourable

But an interpolating formula distinguishes precisely these two cases!

Define ternary SELECTOR function by

$$\text{sel}(x, y, z) = \begin{cases} y & \text{if } x=0 \\ z & \text{if } x=1 \end{cases}$$

This function is not monotone (why?)

We will build interpolating circuit using "gates" $\{\wedge, \vee, \text{sel}\}$.

Later, we will argue that we can replace sel by just \wedge - and \vee -gates to get monotone interpolating circuit.

Next lecture, we will prove following theorem PC1 XIII

THEOREM 5.3 [Pudlak '97]

Suppose $A(\vec{p}, \vec{q}) \wedge B(\vec{p}, \vec{r})$ is unsatisfiable CNF formula over disjoint sets of variables $\vec{p}, \vec{q}, \vec{r}$ and that \exists resolution refutation $\pi: A \wedge B \vdash \perp$ in length L .

Then the following holds:

(1) There is an interpolating circuit $I(\vec{p})$ over $\{\wedge, \vee, \text{sel}\}$ of size $O(L)$.

(2) From π we can construct resolution refutation

(a) $\pi_A: A(\vec{p}, \vec{q}) \vdash \perp$ if $I(\vec{p}) = 0$

(b) $\pi_B: B(\vec{p}, \vec{r}) \vdash \perp$ if $I(\vec{p}) = 1$

in both cases of length $\leq L$

(3) If \vec{p} variables occur only positively in $A(\vec{p}, \vec{q})$ or only negatively in $B(\vec{p}, \vec{r})$, then sel-gates can be replaced by \wedge - and \vee -gates, yielding a MONOTONE circuit of size $O(L)$

If we can prove this theorem, then our proof strategy above yields an exponential lower bound for resolution refutations of clique-colouring formulas

DD2445 COMPLEXITY THEORY: LECTURE 22

RECAP OF LAST LECTURE

Proof complexity: How to prove that CNF formulas are unsatisfiable

Resolution refutation of F

Sequence $\pi = (C_1, C_2, \dots, C_k)$ such that C_k is empty clause \perp and for every $C_i \in \pi$ it holds that

- $C_i \in F$ (axiom), or
- C_i derived from C_j, C_k $j, k < i$ by

RESOLUTION RULE

$$\frac{B \vee x \quad C \vee \bar{x}}{C \vee D}$$

Length/size of refutation = # clauses \perp

Our goal is to prove following theorem

THEOREM (informal). Clique-colouring formulas expressing that there exist n -vertex graphs that are $(m-1)$ -colourable but contain m -cliques are exponentially hard to refute for resolution.

Follows from:

- ① Monotone circuit lower bound for clique
- ② INTERPOLATION technique

THEOREM 2 [Pudlak '97]

Suppose $A(\vec{p}, \vec{q}) \wedge B(\vec{p}, \vec{r})$ is an unsatisfiable CNF formula over disjoint sets of variables $\vec{p}, \vec{q}, \vec{r}$.

Let $\pi: A \wedge B \vdash \perp$ be a resolution refutation in length L . Then the following holds:

- ① There is an INTERPOLATING CIRCUIT $I(p)$ of size $O(L)$ such that
 - (a) $I(p) = 0 \Rightarrow A(p, \vec{q})$ unsatisfiable
 - (b) $I(p) = 1 \Rightarrow B(p, \vec{r})$ unsatisfiable
- ② From π one can construct resolution refutation
 - (a) $\pi_A: A(p, \vec{q}) \vdash \perp$ if $I(p) = 0$
 - (b) $\pi_B: B(p, \vec{r}) \vdash \perp$ if $I(p) = 1$
 in both cases of length $\leq L$
- ③ If \vec{p} -variables occur only positively in $A(\vec{p}, \vec{q})$ or only negatively in $B(\vec{q}, \vec{r})$, then the circuit $I(p)$ can be made monotone

The clique-colouring formula lower bound in Thm 1 follows immediately from Thm 2, as argued last time, so today we focus on Thm 2

Build circuits with \wedge, \vee , and sel-gates
for simplicity.

MI III

$$\text{sel}(x, y, z) = \begin{cases} y & \text{if } x = 0 \\ z & \text{if } x = 1 \end{cases}$$

Proof plan

First do part (2).

Then use (2) to get (1)

Maybe skip details on (3) (but they are in the $L^{\wedge, \vee, \text{sel}}$ lecture notes)

Key definitions (for proof):

g-clause: Clause C over variables \vec{g}
derivable from $A(g, \vec{g})$

r-clause: Clause C over variables \vec{r}
derivable from $B(g, \vec{r})$

Initially true clause I considered to be both
g-clause and r-clause.

Inductive proof ^{of part (2)} From $\pi = (C_1, C_2, \dots, C_L)$
construct sequence of clauses

$\tilde{\pi} = (\tilde{C}_1, \tilde{C}_2, \dots, \tilde{C}_L)$ which will contain
candidate derivations π_A and π_B

Inductive hypothesis

P.1. \tilde{C}_i is a g-clause or an r-clause
Write type $(\tilde{C}_i) = g / r$

P2. $\tilde{C}_i = 1$ only if $C_i \uparrow \mathcal{G} = 1$ | MI IV
 If $\tilde{C}_i \neq 1$, then $\tilde{C}_i \leq C_i$ ~~if $C_i \in \mathcal{G}$~~

P3. If $\tilde{C}_i = 1$ then there is an associated axiom clause E_i such that

- E_i satisfied by $\mathcal{G}(a) = 1$ for $a \in C_i \cap E_i$
- $E_i \in A(\vec{p}, \vec{q})$ if $\text{type}(\tilde{C}_i) = q$
- $E_i \in B(\vec{p}, \vec{r})$ if $\text{type}(\tilde{C}_i) = r$

Think of E_i as justification or excuse why we choose $\tilde{C}_i = 1$

This book-keeping is important for the monotonicity in part 3 (but we won't have time to discuss this in detail, so will not pay too much attention to E_i)

Base case $C_i \in A(\vec{p}, \vec{q}) \wedge B(\vec{p}, \vec{r})$

Set $\tilde{C}_i = C_i \uparrow \mathcal{G}$

$E_i = C_i$ if justification needed

$\text{type}(\tilde{C}_i) = q$ if $C_i \in A$, $= r$ if $C_i \in B$.

Inductive step

$C_i = C \vee D$ derived by resolution rule

$$\frac{C \vee x \quad D \vee \bar{x}}{C \vee D}$$

from $C_j = C \vee x$

$C_k = D \vee \bar{x}$

$j, k < i$

Have already constructed \tilde{C}_j and \tilde{C}_k

$x \in \vec{p} \dot{\cup} \vec{q} \dot{\cup} \vec{r}$. Case analysis:

MI V

Case 1 ($x \in \vec{p}$)

If $g(x) = 0$, set $\tilde{C}_i = \tilde{C}_j$

P1 clearly OK

$\text{type}(\tilde{C}_i) = \text{type}(\tilde{C}_j)$
 $E_i = E_j$ if needed

If $\tilde{C}_i \uparrow g \neq 1$, then

$$\begin{aligned}\tilde{C}_i \uparrow g &\subseteq C_j \setminus \{a, \bar{a} \mid a \in g\} \\ &\subseteq C \setminus \{a, \bar{a} \mid a \in g\} \\ &\subseteq (C \cup D) \setminus \{a, \bar{a} \mid a \in g\} \\ &= C_i \setminus \{a, \bar{a} \mid a \in g\}\end{aligned}$$

If $\tilde{C}_i = 1$, then $C \uparrow g = 1$ since $g(x) = 0$,
so $C_i \uparrow g = (C \cup D) \uparrow g = 1$ and $E_i = E_j$
works as justification axiom.

P2 & P3 OK

If $g(x) = 1$, set

$\tilde{C}_i = \tilde{C}_k$
 $\text{type}(\tilde{C}_i) = \text{type}(\tilde{C}_k)$
 $E_i = E_k$ if needed

Argument same as for $g(x) = 1$.

Case 2 ($x \in \vec{q}$)

MI VI

Divide into subcases depending on types of \tilde{C}_j and \tilde{C}_k

(a) If one of \tilde{C}_j and \tilde{C}_k is r -clause, set \tilde{C}_i to that clause (choose arbitrarily if both are r -clauses).

Set $\text{type}(\tilde{C}_i) = r$ — P1 clearly OK

Copy justification clause to E_i if needed

Observe:

- \tilde{C}_i does not contain any \vec{q} -variables (by IH)
- $x \in \vec{q}$ only variable that disappears in resolution step

therefore P2 & P3 OK.

(b) If \tilde{C}_j or \tilde{C}_k is q -clause not containing x (e.g., if = 1) let $\tilde{C}_i =$ such q -clause without x (choose arbitrarily if both qualify)
Copy justification clause to E_i if needed

Note that since no variable in \vec{p} disappears in resolution step, justification clause ^{may} _(needed is) still OK.

(c) If \tilde{C}_j or \tilde{C}_k q -clause not containing x let \tilde{C}_i be such q -clause. Argue as in (b).

(d) If none of previous cases apply, then M1 VII
 we have q -clauses
 $\tilde{C}_j = \tilde{C}_j' \vee x$ $\tilde{C}_k = \tilde{C}_k' \vee \bar{x}$

both nontrivial (i.e., $\neq 1$)

Let \tilde{C}_i resolvent of these clauses.
 with type $(\tilde{C}_i) = q$

P1 Resolvent of two q -clauses $\Rightarrow q$ -clause

P2
$$\begin{aligned} \tilde{C}_i &= \tilde{C}_j \vee \tilde{C}_k \setminus \{x, \bar{x}\} \\ &\subseteq (C_j \vee C_k \setminus \{a, \bar{a} \mid a \in \mathcal{F}\}) \setminus \{x, \bar{x}\} \\ &= C_i \setminus \{a, \bar{a} \mid a \in \mathcal{F}\} \end{aligned}$$

P3 Vacuously OK since $\tilde{C}_i \neq 1$.

Case 3 ($x \in \vec{r}$)

Analogous.

If one of \tilde{C}_j and \tilde{C}_k is q -clause,
 copy that clause. Otherwise copy
 or construct r -clause. Details
 are left as an exercise.

Part 2 Now follows by induction principle.
 Final clause $C_n = 1$ gets classified as
 q -clause or r -clause $\tilde{C}_n \subseteq C_n = 1$
 means $\tilde{C}_n = 1$. Derived only from
 $A(\mathcal{S}, \vec{q})$ if q -clause or $B(\mathcal{S}, \vec{r})$ if r -clause

Proof of part ①

MI VIII

Go on $\Pi = (C_1, C_2, \dots, C_L = 1)$
Look at construction of $\tilde{\Pi} = (\tilde{C}_1, \tilde{C}_2, \dots, \tilde{C}_L = 1)$
For each C_i construct ^{subcircuit without output} gate v_i computing

$$\text{type}(\tilde{C}_i) = \begin{cases} 0 & \text{if } \tilde{C}_i \text{ g-clause} \\ 1 & \text{if } \tilde{C}_i \text{ r-clause} \end{cases}$$

Just inspect proof of part ②

Base case:

$$C_i \in A(\vec{p}, \vec{q}) \Rightarrow v_i \text{ constant } 0$$

$$C_i \in B(\vec{p}, \vec{r}) \Rightarrow v_i \text{ constant } 1$$

Induction step

Again case analysis over resolution variable $x \in \vec{p} \vee \vec{q} \vee \vec{r}$

Case 1 ($x \in \vec{p}$)

$$\begin{aligned} \text{type}(\tilde{C}_i) &= \text{sel}(x, \text{type}(\tilde{C}_j), \text{type}(\tilde{C}_k)) \\ &= \text{sel}(x, v_j, v_k) \end{aligned}$$

Case 2 ($x \in \vec{q}$)

$$\text{type}(\tilde{C}_i) = 1 \quad \text{if one of } \tilde{C}_j, \tilde{C}_k \text{ has type } 1 \\ \text{otherwise } = 0$$

$$\begin{aligned} \text{type}(\tilde{C}_i) &= \text{type}(\tilde{C}_j) \vee \text{type}(\tilde{C}_k) \\ &= v_j \vee v_k \end{aligned}$$

Case 3 ($x \in \vec{r}$)

MIX

$$\text{type}(\tilde{C}_i) = v_j \wedge v_k$$

Details left as exercise

By the induction principle, final gate v_k will compute $\text{type}(\tilde{C}_k) = \text{type}(1)$

Yields correct interpolating circuit

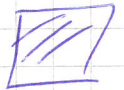
Proof of part ③

Selectors gates are non-monotone.
Need to get rid of them

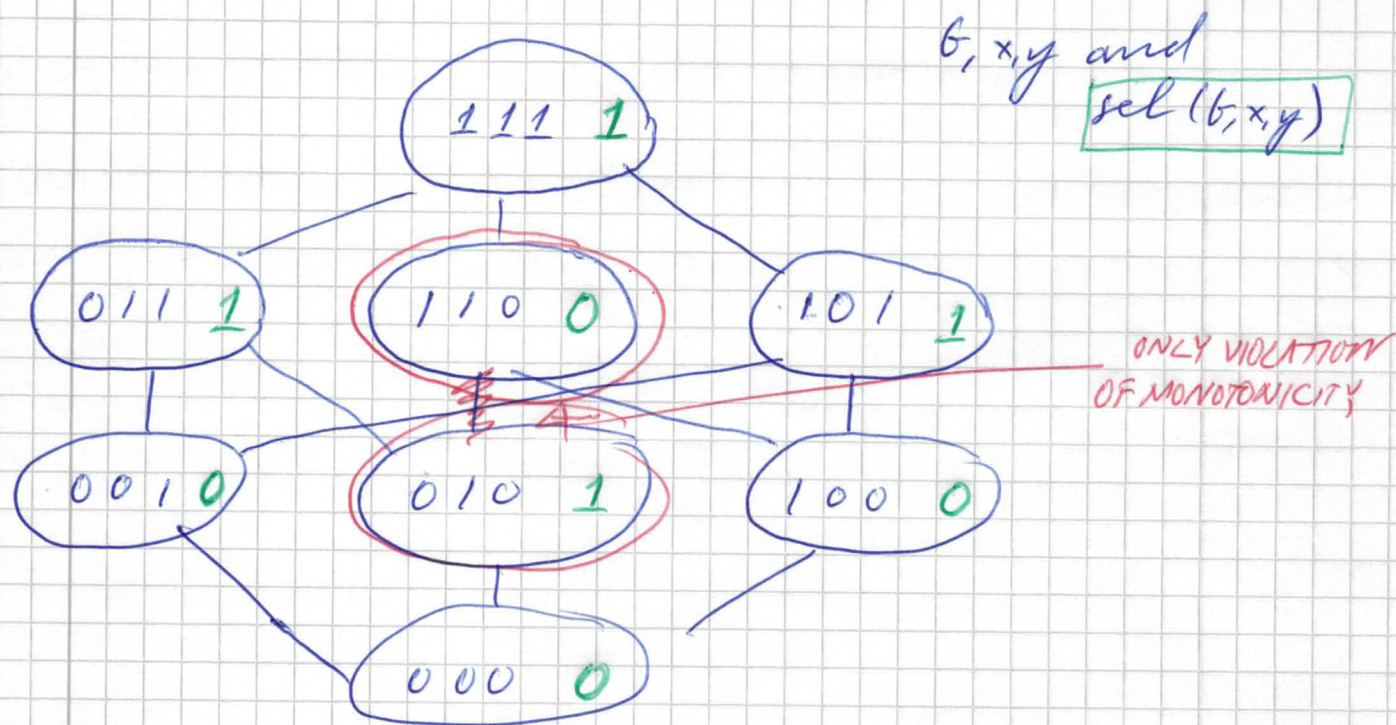
If \vec{p} -variables appear only positively in $A(\vec{p}, \vec{q})$, solution is described in LaTeX-ed notes.

\vec{p} -variables only negative in $B(\vec{p}, \vec{r})$:
Left as exercise.

This is where we use the "justification axioms" \mathcal{E}_i



But the circuit is not monotone, because $sel(b, x, y)$ is not monotone



Suppose wlog \vec{p} only appears positively in $A(\vec{p}, \vec{q})$
(Other case analogous.)

Replace $sel(b, x, y)$ by $(b \vee x) \wedge y$

Only difference for $b=0, x=1, y=0 \mapsto 0$

$\alpha(p_k) = 0$ so we should have picked type from \tilde{C} , which was an r -clause, but instead we are picking q -clause \tilde{D}

WRONG, if \tilde{D} contains \bar{p}_k , because then \tilde{D} is not satisfied but $C \vee D \wedge \vec{x}$ is, and condition

$\tilde{D} \leq C \vee D \wedge \vec{x}$ is violated.

But \tilde{D} cannot contain negative literal \bar{p}_k since it is derived from $A(\vec{p}, \vec{q})$. So this replacement is OK